

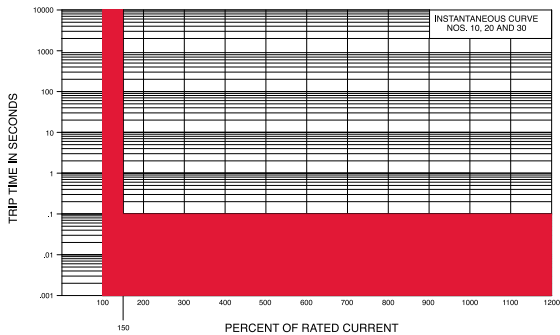
M, MS-SERIES TIME DELAY VALUES										
TRIP TIME SECONDS	PERCENT OF RATED CURRENT									
	Delay	100%	135%	150%	200%	400%	600%	800%	1000%	1200%
	10, 20, 30	No Trip	May Trip	.100 Max	.100 Max	.100 Max	.100 Max	.100 Max	.100 Max	.100 Max
	12, 22, 32, 62, 72, 92	No Trip	.300 - 7.00	.200 - 5.00	.100 - 2.00	.030 - .500	.008 - .300	.006 - .150	.005 - .100	.005 - .100
SECONDS	14, 24, 34, 64, 74, 94	No Trip	3.00 - 70.0	2.00 - 40.0	1.00 - 15.0	.100 - 4.00	.008 - 2.00	.006 - .800	.005 - .350	.005 - .160

Notes:

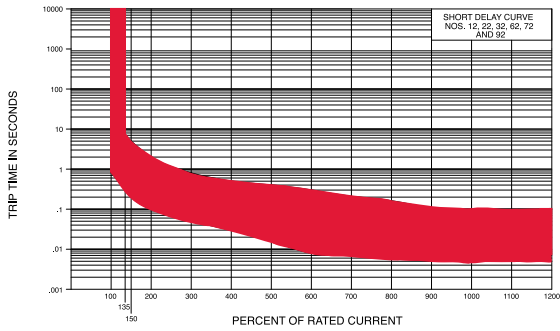
- Delay Curves 12, 14, 22, 24, 32, 34, 62, 64, 72, 74, 92, 94: Breakers to hold 100% and must trip at 135% of rated current and greater within the time limit shown in this curve.
- Delay Curves 10, 20, 30: Breakers to hold 100% and must trip at 150% of rated current and greater within the time limit shown in this curve.
- All Curves: Curve data shown represents breaker response at ambient temperature of 77°F (25°C) with no preloading. Breakers are mounted in standard wall-mount position.
- The minimum inrush pulse tolerance handling capability is 12 times the rated current on standard delays and 18 times the rated current on high inrush delays. These values are based on a 60 Hz 1/2 cycle, 8.33 ms pulse. High inrush delays should be specified for applications with high initial surge currents of short duration, such as switching power supplies, highly capacitive loads and transformer loads.

Dual Rated AC/DC (交/直流)

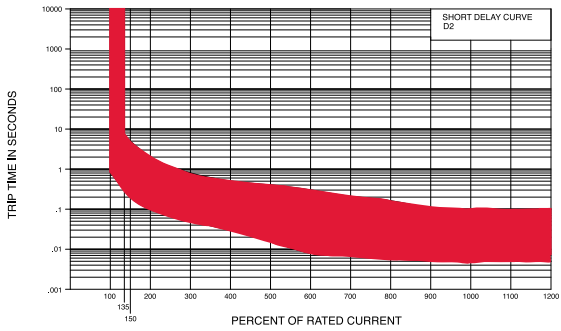
Instantaneous(瞬時)



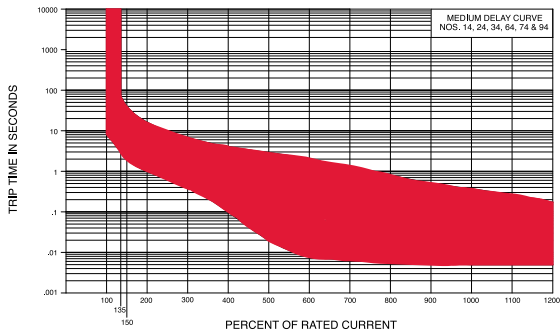
Short(短時)



Short D2(短時D2)



Medium(中時)



Medium D4(中時)

